

ABSTRACT

Disclosed herein is a semiconductor device loading apparatus for test handlers. The semiconductor device loading apparatus includes a body. The apparatus body includes a plurality of pickup cylinders provided with a plurality of vacuum adsorbers for vacuum-sucking and transferring semiconductor devices to be tested, a space adjusting plate for adjusting the pitches of the vacuum adsorbers, and an elevation guiding means for guiding the lifting and lowering of the space adjusting plate. A guide block fixing plate is formed to be separate from the body for guiding the semiconductor devices to be accurately positioned in the pockets of a test tray, respectively.